

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S130	28	((("5313615") or ("5384710") or ("5424589") or ("5452227") or ("5487018") or ("5526277") or ("5550839") or ("5617431") or ("5638380") or ("5644498") or ("5661662") or ("5666289") or ("5774380") or ("5812416"))).PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/04/06 10:16
S131	2	"6425109"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/06 10:18
S132	30	S130 or S131	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/06 10:18
S133	0	S132 and (bdd or (binary adj decision))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/06 10:26
S134	2	boolean and S132	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/06 10:19
S135	0	S132 and (bdd or (decision adj diagram))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/06 10:22
S136	0	S132 and (bdd or (decision adj2 diagram))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/06 10:22
S137	0	S132 and (bdd or (binary adj2 decision))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/06 10:22
S138	0	S132 and (bdd or (binary near3 decision))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/06 10:23
S139	0	S132 and (bdd or (binary with decision))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/06 10:23
S140	13292	((system adj1 on adj1 chip) or SOC) and pin	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/06 14:07

S14 1	13	S140 and (bdd or (binary adj decision))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/06 11:00
S14 2	2	("6477691").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/04/06 10:32
S14 3	16	((("5546562") or ("5910897") or ("5991533") or ("6041170") or ("6134513") or ("6577992"))).PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/04/06 10:51
S14 4	1	S143 and (bdd or (binary adj decision))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/06 10:51
S14 5	114	((system adj1 on adj1 chip) or SOC) and (bdd or (binary adj decision adj diagram))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/06 12:09
S14 6	60	(core or chiplet or block) and S145	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/06 11:42
S14 7	49	(pin or port or terminal or input or output or I/O or input/output) and S146	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/06 11:43
S14 8	7	(pin or port or terminal or input or output or I/O or input/output) near4 (compatib\$5 or mach\$3 or equivalen\$2) and S147	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/06 12:10
S14 9	111	(core or chiplet or block or set or portion) and S145	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/06 12:15
S15 0	2	(pin or port or terminal or input or output or I/O or input/output) with (classify\$3 or sort\$3 or arrang\$3) and S149	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/06 11:44
S15 1	81	(pin or port or terminal or input or output or I/O or input/output) with (classify\$3 or sort\$3 or arrang\$3) and (bdd or (binary adj decision adj diagram))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/06 15:18
S15 2	29	(pin or port or terminal or input or output or I/O or input/output) near4 (compatib\$5 or match\$3 or equivalen\$2 or identical) and S151	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/06 13:57

S15 3	29	(core or chiplet or block or set or portion or cluster) and S152	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/06 12:16
S15 4	2	("5068823").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/04/06 12:57
S15 5	61720	(pin or port or terminal) near4 (compatib\$5 or match\$3 or equivalen\$2 or identical)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/06 14:41
S15 6	48895	(pin or port or terminal) near3 (compatib\$5 or match\$3 or equivalen\$2 or identical)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/06 13:59
S15 7	2020	(pin or port or terminal) near4 (compatib\$5 or match\$3 or equivalen\$2 or identical) with (check\$3 or evaluat\$3 or defin\$3 or assess\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/06 14:29
S15 8	42	((system adj1 on adj1 chip) or SOC) and S157	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/06 14:27
S15 9	4843	(pin or port or terminal or pad) near2 compatib\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/06 14:26
S16 0	92	((system adj1 on adj1 chip) or SOC) and S159	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/06 14:42
S16 1	0	(bdd or obdd or robdd or (binary adj decision)) and S160	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/06 14:42
S16 2	82	(check\$3 or evaluat\$3 or defin\$3 or verif\$7 or assess\$3) and S160	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/06 14:31
S16 3	59042	(pin or port or terminal or pad) near4 (compatib\$5 or match\$3 or identical)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/06 14:47
S16 4	46	(bdd or obdd or robdd or (binary adj decision)) and S163	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/06 14:46

S16 5	2	((system adj1 on adj1 chip) or SOC) and S164	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/06 14:42
S16 6	174	(bdd or obdd or robdd or (binary adj decision)) and ASIC	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/06 14:52
S16 7	10	((pin or port or terminal or pad) near4 (compatib\$5 or match\$3 or identical)) and S166	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/06 14:51
S16 8	405	((pin or port or terminal or pad) near4 (compatib\$5 or match\$3 or identical)) and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/06 14:51
S16 9	8	(bdd or obdd or robdd or (binary adj decision)) and S168	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/06 14:52
S17 0	81	(pin or port or terminal or input or output or I/O or input/output or pad) with (classify\$3 or sort\$3 or arrang\$3) and (bdd or (binary adj decision adj diagram))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/06 15:20
S17 1	34	"716"/\$.ccls. and S170	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/06 15:19
S17 2	20	((classify\$3 or sort\$3 or arrang\$3) with (function\$2 or structur\$2 or electric\$2)) and S171	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/04/06 15:22